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## LISTING OF COMMONLY OWNED APPLICATIONS AND PATENTS 37 CFR 1.78(f)

			37 CFR 1.78(f)	
Attorney Docket Number: QTI.002		First Named Inventor: STACK, Thomas		
Application Number 10/649,885 (if known):			Title: Multiple failure detection shutdown protection circuit for an electronic ballast	
Filing Date 08-26-2003 of the Application:			Classification 315/291 (class and subclass):	
Applicant is providing the listing of commonly owned applications and patents in compliance with 37 CFR 1.78(f)(1) below. If the application or patent also meets the conditions set forth in 37 CFR 1.78(f)(2)(i), applicant must also either: (1) rebut the presumption that the application or patent contains at least one patentably indistinct claim; or (2) submit a terminal disclaimer in accordance with 37 CFR 1.321(c). In addition, if applicant files a terminal disclaimer for a pending application in the above-identified application, applicant must provide an explanation why there are two or more pending nonprovisional applications containing patentably indistinct claims. See 37 CFR 1.78(f)(2)(ii)(B).				
If rebuttable presumption under 37 CFR 1.78(f)(2) also exists			Application Numbers under 37 CFR 1.78(f)(1)	Patent Numbers
Rebuttal attached	Terminal disclaimer attached	Explanation	(Required for applications and patents)	(Required for patents)
	· 🗆		11/105,091	
Signature OJG			Da	November 2, 2007
Name (Print/Typed) D. James Chung			Re	41,678 egistration Number
Note: Signatures of all the inventors or assignees of record of the entire interest or their representative(s) are required in accordance with 37 CFR 1.33 and 10.18. Please see 37 CFR 1.4(d) for signature requirements. If necessary, submit multiple forms for more than one signature, see below*.				
*Total of forms are submitted.				

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The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.11 and 1.14. This collection is estimated to take 1 hour to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.